

TIME TABLE FOR 3RD & 5TH SEMESTER ETC. ENGG. (2023-2024) (DEPARTMENT OF ETC ENGG)

DAYS	SEMESTER	1st (9.00-9.55)	2nd (9.55-10.50)	3rd (10.50-11.45)	4th (11.45-12.40)	(12.40-1.20)	5th (1.20-2.15)	6th (2.15-3.10)	7th (3.10-4.05)
MONDAY	3rd	ES.(SS)	DE(AKM)	CNT(SKD)	EM&I (DKB)	R E C E S S	DE LAB(AKM/MB)		EM&I (DKB)
	5th	PE&PLC(RKS)	WP&BCE(JS)	E&M&ST(RRL)	A&DC(RKS))		PE & PLC(RKS/SKG)		
TUSEDAY	3rd	MATH-III (RKB/PS)	CP&WPD LAB(SRP/TRS)		EM&I (DKB)		DE(AKM)	CNT(SKD)	ES.(SS)
	5th	WP&BCE(JS)	E&M&ST(RRL)	VLSI&ES(SRP)	PE and PLC (RKS)		VLSI LAB(SRP/MB)		
WEDNESDAY	3rd	C&S LAB(SKD/SKG)		EM&I LAB.(DKB/SKG)			MATHIII(RKB/PS)	CNT(SKD)	ES.(SS)
	5th	VLSI&ES(SRP)	A&DC(RKS)		E&M&ST(RRL)		A&DC LAB(RKS/MB)		
THURSDAY	3rd	C&S LAB(SKD/SKG)		DE(AKM)	EM&I (DKB)		EM&I LAB(DKB/SKG)		DE(AKM)
	5th	A&DC(RKS)	VLSI&ES(SRP)	WP&BCE(JS)	E&M&ST(RRL)		WP&BCE LAB(JS/MB)		
FRIDAY	3rd	MATH-III (RKB/PS)	CNT(SKD)	CP&WPD LAB(SRP/TRS)			Library(DKB/SKG)	SCA(DKB/SKG)	
	5th	A&DC(RKS)	WP&BCE(JS)	VLSI&ES(SRP)	PE&PLC(RKS)		PROJECT PHASE-1(ARD/SBP)		
DAY	SEMESTER	(9.00-9.55) 1st	(9.55-10.50) 2nd	(10.50 -11.45) 3rd	(11.45-12.40) 4th	(12.40-1.35) 5th			
SATURDAY	3rd	ES.(SS)	EM&I (DKB)	DE LAB(AKM/MB)		MATH-III (RKB/PS)			
	5th	VLSI&ES(SRP)	PE & PLC(RKS)	SCA(RKS/SBP)	Library(RKS/SBP)	PROJECT PHASE-1(ARD/SBP)			

PRINCIPAL

Memo No. _____ Date:-17.07.2023

Copy to :Principals' office, Notice Boards, OIC(Admin)/
OIC(Exam)/All Sr. Lecturers for information & necessary action

Sr. Lecturer
Electrical & Electronics Department